**Weekly Project Report (Week 3 – 10/15)**

1. **Last week:**

* Review and submit homework 2
* Figure out mentor graphic is available in Inter lab
* Discuss about project proposal
* Completed the draft for Design Architecture
* Standard packet size will not exceed 5”x5”x5”

1. **Next week**

* Discuss and review about Design Architecture.
* Decide which program is used to make schematic capture.

1. **Issue:**

* Unable to choose microcontroller
* Unable to choose types of audio